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Book Review | Digital Logic and computer Design by Morris Mano | Digital Electronics book Review

~~Book M Morris Mano index Q. 1.20: Convert decimal +49 and +29 to binary, using the signed 2's complement representation and e Q. 1.33: The state of a 12-bit register is 100010010111. What is its content if~~

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it represents

Boolean Logic \u0026amp; Logic Gates: Crash Course Computer Science #3Q.

6.24: Design a counter with T flip-flops that goes through the following binary repeated sequence Q. 3.17: Draw a NAND logic diagram that implements the complement of the following function

One MUST READ book on Digital Electronics | Digital Logic and Computer Design | video in HINDI Q. 3.15: Simplify the following Boolean function F , together with the don't-care conditions d , and Q. 1.25:

Represent the decimal number 5137 in (a) BCD (b) excess-3 code (c) 2421 code (d) a 6311 code Q. 3.8: Find the minterms of the following Boolean expressions by plotting each function in a map Q. 2.19:

Express following function as sum of minterms and product of maxterms: $F = B'D + A'D + BD$ Q. 3.6: **Simplify the following Boolean expressions, using four-variable maps: (a) $A'B'C'D' + AC'D' + Q. 1.15: Find the 9's and the 10's complement of the following decimal numbers: (a)$**

52, 784, 630 Q. 3.4: Simplify the following Boolean functions, using Karnaugh maps: (a) $F(x, y, z) = \sum(2, 3, 6, 7)$

Q. 3.13: Simplify the following expressions to (1) sum-of-products and (2) products-of-sums Q. 4.4: Design a combinational circuit with three inputs and one output. (a) The output is 1 when Q. 3.12: Simplify the following Boolean functions to product-of-sums form: (a)

$F(w, x, y, z) = \sum(0, 1, 2,$

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Binary Addition and Subtraction With Negative Numbers, 2's Complements
\u0026 Signed Magnitude

Q. 4.1: Consider the combinational circuit shown in Fig. P4.1.(a)*
Derive the Boolean expressions fo

L2 Introduction to combinational circuit *Digital Design: Q. 1.13: Do
the following conversion problems: (a) Convert decimal 27.315 to
binary*

Q. 3.14: Give three possible ways to express following Boolean
function with eight or fewer literals **Digital Design by MORRIS MANO.flv**

~~Q. 3.16: Simplify the following functions, and implement them with two-
level NAND gate circuits: Q. 3.9: Find all the prime implicants for
the following Boolean functions, and determine which are Morris Mano
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